

Amendments to the Claims:

In the claims:

Please AMEND Claims 1, 3-9, 11-13 and 16-17 as follows, and ADD Claims 18-46 such that the pending claims will read as follows:

[c1] Claim 1 (Currently Amended): An apparatus for transferring digital data at a high rate between a host device and a client device over a communication path for presentation to a user, comprising:

means for generating one or more of a plurality of pre-defined packet structures and linking them together to form a pre-defined communication protocol;

means for communicating a pre-selected set of digital control and presentation data between said host device and said client device[[s]] over said communication path using said communication protocol;

means for coupling at least one host link controller residing in said host device to said client device through said communications path, the host link controller being configured to generate, transmit, and receive packets forming said communications protocol, and to form digital presentation data into one or more types of data packets; and

means for transferring data in the packet form ~~of packets~~ over said communications path using said link controller[[s]].

[c2] Claim 2 (Original): The apparatus of Claim 1 further comprising means for grouping said packets together within media frames for communication between said host and client, the media frames having a pre-defined fixed length with a pre-determined number of said packets have differing and variable lengths.

[c3] Claim 3 (Currently Amended): The apparatus of Claim 2 further comprising:

means for negotiating between said host and client link drivers the use of one of a plurality of transfer modes in each direction, each allowing the transfer of a different maximum number[[s]] of bits of data in parallel over a given time period; and

means for dynamically adjusting between said transfer modes during transfer of data.

[c4] Claim 4 (Currently Amended): The apparatus of Claim 1 further comprising means for commencing transfer of packets from said host device with a Sub-frame Header type packet.

[c5] Claim 5 (Currently Amended): The apparatus of Claim 1 further comprising means for transferring information between said host and client devices bi-directionally over said communications link path.

[c6] Claim 6 (Currently Amended): The apparatus of Claim 1 further comprising means for transferring data from said host device to said client device for presentation to a client user using one or more Video Stream type packets for video type data, and Audio Stream type packets for audio type data.

[c7] Claim 7 (Currently Amended): The apparatus of Claim 1 further comprising means for transferring data from said client device to said host device using one or more Reverse Link Encapsulation type packets.

[c8] Claim 8 (Currently Amended): The apparatus of Claim 1 further comprising means for requesting display capabilities information

from the client device by ~~[[a]]~~ the host link controller so as to determine what type of data and data rates said client device is capable of accommodating through said interface.

[c9] Claim 9 (Currently Amended): The apparatus of Claim 8 further comprising means for communicating the display capabilities information or presentation capabilities information from a client link controller to said host link controller using at least one Display Capability type packet.

[c10] Claim 10 (Original): The apparatus of Claim 1 wherein said communication path comprises a cable having a series of four or more conductors and a shield.

[c11] Claim 11 (Currently Amended): The apparatus of Claim 1 further comprising means for operating a USB data interface by ~~each of~~ said link controller~~[[s]]~~ as a part of said communication path.

[c12] Claim 12 (Currently Amended): The apparatus of Claim 1 further comprising means for storing multimedia data to be transferred to said client device at said host device.

[c13] Claim 13 (Currently Amended): The apparatus of Claim 1 further comprising means for generating Filler type packets by said host device to occupy periods of forward link transmission that do not have data.

[c14] Claim 14 (Original): The apparatus of Claim 1 further comprising means for transferring interface-user defined data using User-Defined Stream type packets.

[c15] Claim 15 (Original): The apparatus of Claim 1 further comprising means for transferring data to or from user input devices associated with said client device using Keyboard Data and Pointing Device Data type packets.

[c16] Claim 16 (Currently Amended): The apparatus of Claim 1 further comprising means for terminating [[the]] transfer of data in either direction over said communication path using a Link Shutdown type packet for transmission by said host device to said client device.

[c17] Claim 17 (Currently Amended): A communication system for transferring digital data at a high rate between a host device and a client device over a communication path, the system comprising:
a processor, said processor configured to generate one or more of a plurality of pre-defined packet structures and link them together to form a pre-defined communication protocol; to form digital presentation data into one or more types of data packets; communicate a pre-selected set of digital control and presentation data between said host device and said client device[[s]] over the communication path using the communication protocol; and transfer data in the packet form ~~of packets~~ over the communication[[s]] path.

Claim 18 (New): A method for power reduction in a digital data interface communication data link, the method comprising:
sending a link shut down packet from a host device to a client device;
disabling a data driver to a high impedance state by the host device to place the digital data interface communication data link in hibernation;

providing a logic one to a data line to drive the data line to a logic one state;

toggling a strobe for a first predetermined period of time;

driving the data line to a zero state and toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and

transmitting a sub-frame header packet.

Claim 19 (New): The method of claim 18 wherein disabling the data driver comprises producing a high impedance state to define a zero logic state while the communication data link is in hibernation.

Claim 20 (New): The method of claim 18 wherein providing a logic one to the data line comprises providing the logic one by the host device.

Claim 21 (New): The method of claim 18 wherein the data driver comprises a strobe driver.

Claim 22 (New): The method of claim 18 wherein providing a logic one to the data line comprises providing the logic one by the client device causing the host device to drive the data line to the logic one.

Claim 23 (New): The method of claim 18 further comprising conserving power during a time when data is not being transferred to and from the host device and client device.

Claim 24 (New): A method of placing a digital data interface communication data link in a hibernation mode, the method comprising:

- sending a link shut down packet from a host device to a client device; and

- disabling a data driver to a high impedance state by the host device.

Claim 25 (New): The method of claim 24 wherein disabling the data driver comprises producing a high impedance state to define a zero logic state while the communication data link is in hibernation.

Claim 26 (New): The method of claim 25 wherein the data driver comprises a strobe driver.

Claim 27 (New): A method of restarting a digital data interface communication data link from a hibernation mode, the method comprising:

- providing a logic one to a data line to drive the data line to a logic one state;

- toggling a strobe for a first predetermined period of time;

- driving the data line to a zero state and toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and

- transmitting a sub-frame header packet.

Claim 28 (New): The method of claim 27 wherein the data driver comprises a strobe driver.

Claim 29 (New): The method of claim 27 wherein providing a logic one to the data line comprises providing the logic one by the host device.

Claim 30 (New): The method of claim 27 wherein providing a logic one to the data line comprises providing the logic one by the client device causing the host device to drive the data line to the logic one.

Claim 31 (New): A computer program product, comprising:
computer readable medium comprising:

- code for causing a link shut down packet in a digital data interface communication data link to be sent from a host device to a client device;

- code for causing a data driver to be disabled to a high impedance state by the host device to place the digital data interface communication data link in hibernation;

- code for causing a logic one to be provided to a data line to drive the data line to a logic one state;

- code for causing a strobe to be toggled for a first predetermined period of time;

- code for causing the data line to be driven to a zero state and toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and

code for causing a sub-frame header packet to be transmitted.

Claim 32 (New): A computer program product, comprising:
computer readable medium comprising:

code for causing a link shut down packet in a digital data interface communication data link to be sent from a host device to a client device; and

code for causing a data driver to be disabled to a high impedance state by the host device.

Claim 33 (New): A computer program product, comprising:
computer readable medium comprising;

code for causing a logic one to a data line to drive the data line to a logic one state for restarting a digital data interface communication data link from a hibernation mode;

code for causing a strobe to be toggled for a first predetermined period of time;

code for causing the data line to be driven to a zero state and for toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and

code for causing a sub-frame header packet to be transmitted.

Claim 34 (New): An apparatus for power reduction in a digital data interface communication data link, the apparatus comprising:

means for sending a link shut down packet from a host device to a client device;

means for disabling a data driver to a high impedance state by the host device to place the digital data interface communication data link in hibernation;

means for providing a logic one to a data line to drive the data line to a logic one state;

means for toggling a strobe for a first predetermined period of time;

means for driving the data line to a zero state and toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and

means for transmitting a sub-frame header packet.

Claim 35 (New): The apparatus of claim 34 wherein the means for disabling the data driver comprises means for producing a high impedance state to define a zero logic state while the communication data link is in hibernation.

Claim 36 (New): The apparatus of claim 34 wherein the means for providing a logic one to the data line comprises means for providing the logic one by the host device.

Claim 37 (New): The apparatus of claim 34 wherein the data driver comprises a strobe driver.

Claim 38 (New): The apparatus of claim 34 wherein means for providing a logic one to the data line comprises means for providing the logic one by the client device causing the host device to drive the data line to the logic one.

Claim 39 (New): The apparatus of claim 34 further comprises means for conserving power during a time when data is not being transferred to and from the host device and client device.

Claim 40 (New): An apparatus for placing a digital data interface communication data link in a hibernation mode, the apparatus comprising:

means for sending a link shut down packet from a host device to a client device; and

means for disabling a data driver to a high impedance state by the host device.

Claim 41 (New): The apparatus of claim 40 wherein the means for disabling the data driver comprises means for producing a high impedance state to define a zero logic state while the communication data link is in hibernation.

Claim 42 (New): The apparatus of claim 41 wherein the data driver comprises a strobe driver.

Claim 43 (New): An apparatus for restarting a digital data interface communication data link from a hibernation mode, the apparatus comprising:

means for providing a logic one to a data line to drive the data line to a logic one state;

means for toggling a strobe for a first predetermined period of time;

means for driving the data line to a zero state and toggling the strobe for a second predetermined period of time to wake up the digital data interface communication data link; and means for transmitting a sub-frame header packet.

Claim 44 (New): The apparatus of claim 43 wherein the data driver comprises a strobe driver.

Claim 45 (New): The apparatus of claim 43 wherein the means for providing a logic one to the data line comprises means for providing the logic one by the host device.

Claim 46 (New): The apparatus of claim 43 wherein the means for providing a logic one to the data line comprises means for providing the logic one by the client device causing the host device to drive the data line to the logic one.